

ABSTRACT

The present invention relates to a liquid crystal display having no gate PCB and FPC. This comprises: a liquid crystal panel formed by an assemble an array substrate and a color filter substrate with a liquid crystal layer interposed therebetween; a plurality of source TCPs and gate TCPs, which are attached to a source and a gate of the liquid panel, respectively, and mounted with a gate driver IC and a source driver IC, respectively; a source PCB connected to the source TCPs and serving to apply a given driving signal to the source driver IC; a LOG formed on the liquid crystal panel such that a signal from the source PCB is applied to the gate driver IC via the gate TCPs; and transfers formed between the substrates at edges of the liquid crystal panel such that common voltage is applied to the color filter substrate. In this liquid crystal display, the LOG consists of gate driver IC-driving signal lines without having a common line for connecting the transfers with each other. According to the present invention, the common line is eliminated from the LOC so that a margin in a LOC layout can be ensured. This can reduce the resistance of the LOC to a suitable level and thus improve the screen quality of the liquid crystal display.